

International Telecommunication Union

**ITU-T**

TELECOMMUNICATION  
STANDARDIZATION SECTOR  
OF ITU

**G.703**

**Corrigendum 1**  
(03/2008)

SERIES G: TRANSMISSION SYSTEMS AND MEDIA,  
DIGITAL SYSTEMS AND NETWORKS

Digital terminal equipments – General

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Physical/electrical characteristics of hierarchical  
digital interfaces

**Corrigendum 1**

ITU-T Recommendation G.703 (2001) – Corrigendum 1



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# **ITU-T Recommendation G.703**

## **Physical/electrical characteristics of hierarchical digital interfaces**

### **Corrigendum 1**

#### **Summary**

This corrigendum contains material to correct ITU-T Recommendation G.703 (2001), *Physical/electrical characteristics of hierarchical digital interfaces*.

#### **Source**

Corrigendum 1 to ITU-T Recommendation G.703 (2001) was approved on 29 March 2008 by ITU-T Study Group 15 (2005-2008) under the ITU-T Recommendation A.8 procedure.

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# ITU-T Recommendation G.703

## Physical/electrical characteristics of hierarchical digital interfaces

### Corrigendum 1

Modify Table 11 as shown:

**Table 11/G.703 – Digital 2048 kHz clock interface**

Pulse shape	The signal must conform with the mask (Figure 20). The value V corresponds to the maximum peak value. The value $V_1$ corresponds to the minimum peak value.	
Type of pair	Coaxial pair (see Note in 13.4)	Symmetrical pair (see Note in 13.4)
Test load impedance	75 ohms resistive	120 ohms resistive
Maximum peak voltage ( $V_{ep}$ )	1.5	1.9
Minimum peak voltage ( <del><math>V_{ep}</math></del> ) ( $V_1$ )	0.75	1.0
Maximum jitter at an output port	Refer to Table 5/G.823 (Note)	
NOTE – This value is valid for network timing synchronization equipments. Other values may be specified for timing output ports of digital links carrying the network timing.		





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